## PS-5-1



## A Characterization Method of On-Chip Tee-Junction for Millimeter-Wave CMOS Circuit Design

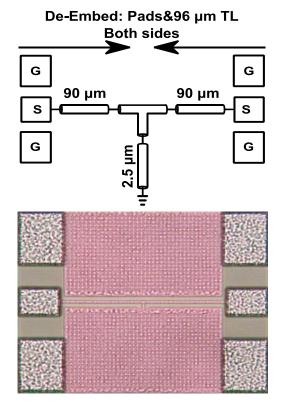
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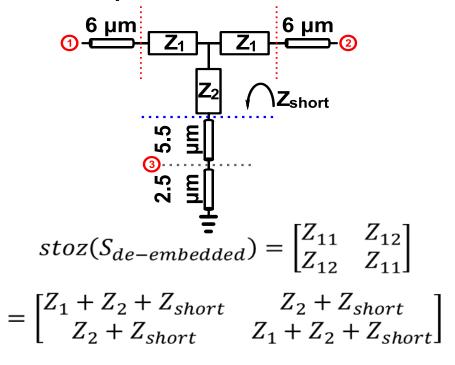


## **Tee-Junction Characterization**

- PS-5-1
- Millimeter-Wave CMOS circuits for low-cost, highdata-rate wireless transceivers
- Device Characterizations for design accuracy
- Characterization



Proposed Model





**Pursuing Excellence** 

Verification Structure

Conventional Model

